

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Steven L. Scott	Examiner:	Sheng Tsai
Serial No.:	10/643,585	Group Art Unit:	2186
Filed:	August 18, 2003	Docket No.:	1376.700US1
Customer No.:	21186	Confirmation No.:	4004
Title:	LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER		

AMENDMENT & RESPONSE UNDER 37 C.F.R. § 1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Office Action mailed September 15, 2008, please amend the application as follows.